IN THE CLAIMS

Please amend the claims as follows:

Claims 1-7 (Canceled).

Claim 8 (Original): A semiconductor device for driving a switching device having a first electrode, a second electrode, and a control electrode, said semiconductor device comprising:

- a first terminal connected to said first electrode;
- a second terminal connected to said first electrode through a capacitive element;
- a semiconductor substrate having a first conductivity type;
- a first impurity region formed in a main surface of said semiconductor substrate and having a second conductivity type;
- a second impurity region formed in a main surface of said first impurity region and having said first conductivity type;
- a first transistor having a source/drain region of said conductivity type, said source/drain region of said first transistor being formed in a main surface of said second impurity region and connected to said first terminal;
- a second transistor having a source/drain region of said first conductivity type, said source/drain region of said second transistor being formed in said main surface of said first impurity region and connected to said second terminal; and
- at least one of a third impurity region formed in said main surface of said first impurity region, having said first conductivity type, and connected to said first terminal and a fourth impurity region formed in said main surface of said first impurity region, having said first conductivity type, and connected to said second terminal.

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Claim 9 (Original): The semiconductor device according to claim 8, further comprising an isolation region having said first conductivity type and formed in said main surface of said semiconductor substrate and in contact with said first impurity region,

wherein said fourth impurity region is formed between isolation region and said second impurity region and surrounds said second impurity region continuously or intermittently.

Claims 10-20 (Canceled).